Amendment t the Abstract:

The Abstract has been amended. A revised Abstract is attached.

ABSTRACT

The present invention provides an oscillator or PLL circuit which can balance the characteristics of a circuit without being affected by noise from a signal line or a supply line. ~~The present invention provides~~ There is provided an oscillator comprising a resonance circuit has a first series connected circuit having coils and a power terminal, a second series connected circuit having capacitors and a varactor having directional characteristics, and a third series connected circuit having capacitors and a varactor having directional characteristics. The first, second, and third series connected circuits are connected in parallel. The varactors are connected so as to have opposite directionalities with respect to a connection side of the second and third series connected circuit. The capacities of the varactors are varied by external control. The varied capacities determine an oscillation frequency.

Respectfully submitted,

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Attachment: Abstract

Dated: July 15, 2003

The Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. **18-0350** of any fees associated with this communication.	**EXPRESS MAIL: Mailing Label Number: EV 331 707 640 US** **Date of Deposit: July 15, 2003** I hereby certify that this paper and fee are being deposited, under 37 C.F.R. § 1.10 and with sufficient postage, using the "Express Mail Post Office to Addressee" service of the United States Postal Service on the date indicated above and that the deposit is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. Kathleen Libby **KATHLEEN LIBBY**

ABSTRACT

The present invention provides an oscillator or PLL circuit which can balance the characteristics of a circuit without being affected by noise from a signal line or a supply line. There is provided an oscillator comprising a resonance circuit has a first series connected circuit having coils and a power terminal, a second series connected circuit having capacitors and a varactor having directional characteristics, and a third series connected circuit having capacitors and a varactor having directional characteristics. The first, second, and third series connected circuits are connected in parallel. The varactors are connected so as to have opposite directionalities with respect to a connection side of the second and third series connected circuit. The capacities of the varactors are varied by external control. The varied capacities determine an oscillation frequency.